

Preliminary Amendment

Applicant: Alessandro Minzoni et al.

Serial No.: Unknown

(Priority Application No. DE 103 45 491.8)

(International Application No. PCT/EP2004/052126)

Filed: Herewith

(Priority Date: September 30, 2003)

(International Filing Date: September 10, 2004)

Docket No.: I438.111.101

Title: CLOCK RECEIVER CIRCUIT DEVICE, IN PARTICULAR FOR SEMI-CONDUCTOR COMPONENTS

IN THE ABSTRACT

Please replace the Abstract with the following rewritten paragraph:

**CLOCK RECEIVER CIRCUIT DEVICE, IN PARTICULAR FOR SEMI-
CONDUCTOR COMPONENTS**

Abstract

The invention relates to A a semi-conductor component with a receiver, in particular a clock receiver circuit device ~~(1)~~, as well as a receiver, in particular a clock receiver circuit device is disclosed. The clock receiver circuit device includes ~~(1)~~, with a first input (9a) adapted to be connected with a first connection ~~(3a)~~ of a semi-conductor component, and second input ~~(8a)~~ adapted to be connected with a second connection ~~(3b)~~ of the semi-conductor component, wherein

~~characterized in that~~

the receiver circuit device ~~(1)~~ comprises includes several, in particular more than three transfer gates ~~(4, 5, 6, 7)~~.

~~Figure 1~~